

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800**

In re application of:

Issy Kipnis et al.

Application No: 10/816,264

Filed: March 31, 2004

For: SEMICONDUCTOR SUBSTRATE)
WITH INTERCONNECTIONS)
AND EMBEDDED CIRCUIT)
ELEMENTS)

Examiner: Victor A. Mandala

Art Unit: 2826

Confirmation No.: 9836

Mail Stop AF

Assistant Commissioner For Patents

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RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action mailed July 17, 2007, Applicant respectfully requests reconsideration of the above-referenced patent application in light of the following.

Claim Listing including amendments begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

Conclusion begins on page 6 of this paper.

Filed via EFS Web on September 17, 2007